



FORM PTO-1449				ATTY. DOCKET NO. I-2-0183.1US		SERIAL NO. 10/046,601	
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				APPLICANT Edward L. Helper			
				FILING DATE October 23, 2001		GROUP 2131	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)							
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
KA		5,311,176	05/1994	Gurney			
		5,602,833	02/1997	Zehavi			
		5,751,761	05/1998	Gilhousen			
		6,014,408	01/2000	Naruse et al.			
		6,091,757	06/2000	Cudak et al.			
		6,115,410	09/2000	Naruse			
		6,262,751	07/2001	Chan			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
KA		01/58070	08/2001	WO			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
KA		Kung, "VLSI Array Processors", 1998, pp. 436-438.					

EXAMINER /Kaveh Abrishamkar/	DATE CONSIDERED 08/24/2006
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.